

# Notice of Allowability

Application No.

10/511,836

Examiner

Thuy V. Tran

Applicant(s)

GEORGE, JOHN BARRETT

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## -- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 10/19/2004 incl. prel. amendment concurrently filed.
2. ☒ The allowed claim(s) is/are 1-12.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
  - a) ☐ All    b) ☐ Some\*    c) ☐ None    of the:
  1. ☐ Certified copies of the priority documents have been received.
  2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.
  3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

\* Certified copies not received: \_\_\_\_\_

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  
**THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.**

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
  - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
    - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date \_\_\_\_\_.
  - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date \_\_\_\_\_.Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

### Attachment(s)

1. ☒ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☒ Information Disclosure Statements (PTO-1449 or PTO/SB/08),  
Paper No./Mail Date 10/19/2004
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413),  
Paper No./Mail Date \_\_\_\_\_
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other \_\_\_\_\_

  
**THUY V. TRAN**  
**PRIMARY EXAMINER**

### **DETAILED ACTION**

This is a response to the Applicant's filing on 10/19/2004 and preliminary amendment concurrently filed therewith. In virtue of this amendment, claims 1-12 are currently presented in the instant application.

#### ***Information Disclosure Statement***

1. The information disclosure statement (IDS) submitted on 10/19/2004 is in compliance with the provisions of 37 CFR 1.97. Accordingly, the information disclosure statement is being considered by the examiner.

#### ***Drawings***

2. The drawings submitted on 10/19/2004 are accepted.

#### ***Allowable Subject Matter***

3. Claims 1-12 are allowed.

#### ***Reasons for Allowance***

4. The following is an examiner's statement of reasons for allowance:

Prior art fails to disclose or fairly suggest:

- A waveform generator comprising a second semiconductor switch responsive to said level shifted periodic input correction signal and to said periodic switch control signal for generating a periodic output dynamic correction signal having a frequency related to a deflection frequency and a waveform portion, controlled by an operation of said second semiconductor switch, said waveform portion occurring, during a corresponding portion of a period of said output dynamic correction signal, said dynamic correction signal being coupled to a cathode ray tube to vary a field in a

beam path of an electron beam of said cathode ray tube for providing dynamic correction, in combination with the remaining claimed limitations as called for in independent claim 1 (claims 2-9 are allowed since they are dependent on claim 1);

- A waveform generator for providing dynamic correction comprising a second semiconductor switch responsive to a signal at a second reference level and to said periodic switch control signal for periodically clamping said signal developed at said second terminal, in accordance with said second reference level, said signal developed at said second terminal being coupled to a cathode ray tube to vary a field in a beam path of an electron beam of said cathode ray tube for providing dynamic correction, in combination with the remaining claimed limitations as called for in independent claim 10 (claim 11 is allowed since it is dependent on claim 10); and
- A waveform generator comprising a second semiconductor switch responsive to said periodic switch control signal and to said level shifted input correction signal and coupled to said output of said amplifier that isolates said second semiconductor switch from said capacitor for generating a periodic output dynamic correction signal having a first portion, during a first portion of a period of said input correction signal, that is controlled in accordance with said periodic input correction signal, and a second portion, during a second portion of said period of said input correction signal, that is controlled, in accordance with an operation of said second semiconductor switch, said dynamic correction signal being coupled to a cathode ray tube to vary a field in a beam path of an electron beam of said cathode ray tube for providing

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dynamic correction, in combination with the remaining claimed limitations as called for in independent claim 12.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

***Citation of relevant prior art***

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Prior art George (U.S. Patent No. 5,747,948) discloses a convergence correction.

Prior art George (U.S. Patent No. 5,430,358) discloses a high voltage vertical dynamic focus amplifier.

Prior art Golik (U.S. Patent No. 5,162,705) discloses a dynamic focusing circuit for CRT.

Prior art Fernsler et al. (U.S. Patent No. 5,034,664) discloses parabola generators.

Prior art Valkestijn (U.S. Patent No. 4,216,392) discloses a circuit for producing a periodic parabolic signal.

***Inquiry***

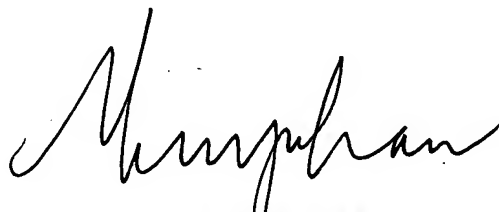
Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuy V. Tran whose telephone number is (571) 272-1828. The examiner can normally be reached on M-F (8:00 AM -5:00 PM).

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Don Wong can be reached on (571) 272-1834. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

11/13/2005



**THUY V. TRAN**  
**PRIMARY EXAMINER**